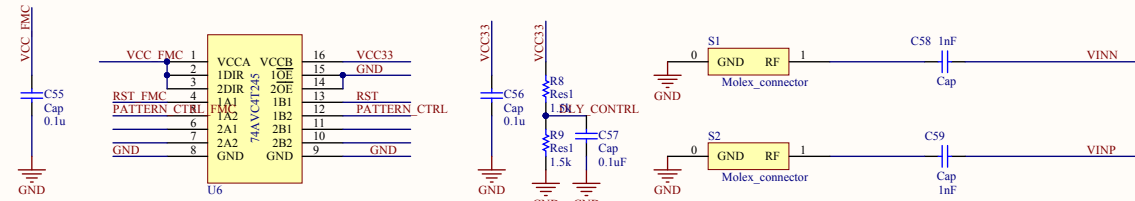
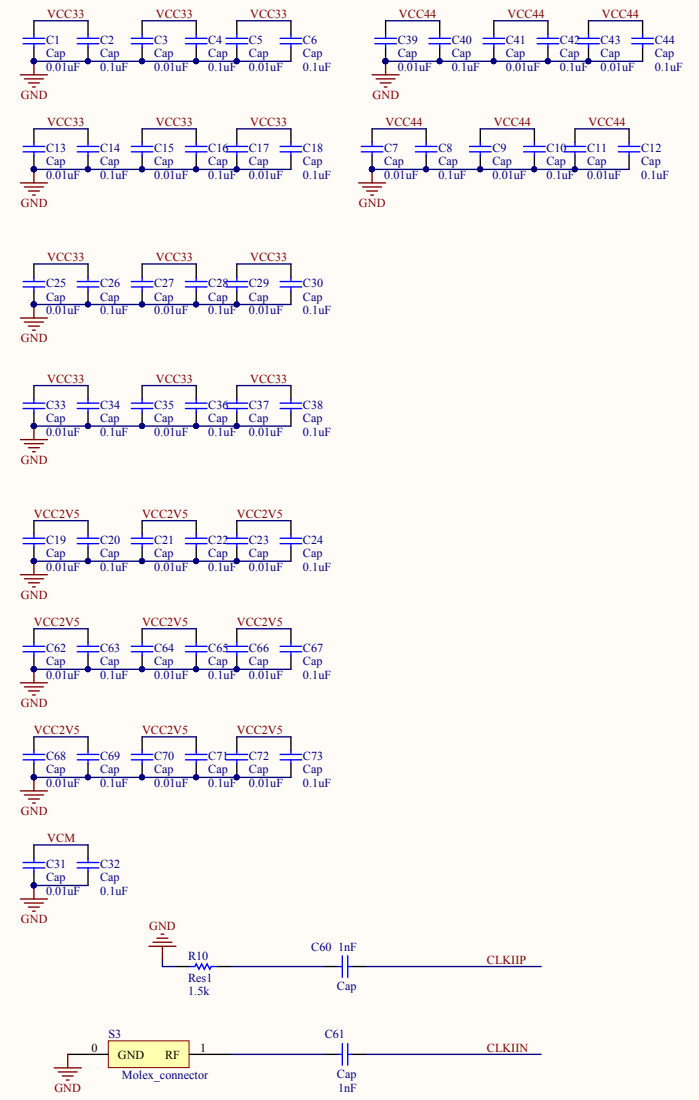


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版本号		日期		修订记录		版本号	日期	修订记录
Rev-2.0		2019/4/28		原理图模板变更				
A							A	
B							B	
C							C	
D							D	

 Acelo Micro 苏州迅芯微电子有限公司	
Project Title: AAD06S9000LEVM	
Sheet Title: 1	
Size: A3	Revision: Rev-2.0
DrawnBy: QXL	CheckedBy: ZL
Sheet 1 of 4	Date: 2019/4/28

GND	A1	GND	A1	VOP2_D4	M16	VOP2_D4
GND	A2	GND	A2	VON2_D4	M15	VON2_D4
VOP1_A1	A3	VOP1_A1	A3	M14	GND	M14
VOP1_B1	A4	VOP1_B1	A4	M13	GND	M13
VOP2_A1	A5	VOP2_A1	A5	M12	VCC2V5	M12
GND	A6	GND	A6	M11	VCC2V5	M11
VINN	A7	VINN	A7	M10	VCC2V5	M10
VINP	A8	VINP	A8	M9	VCC2V5	M9
GND	A9	GND	A9	M8	VCC2V5	M8
CLKHP	A10	CLKHP	A10	M7	VCC2V5	M7
CLKHN	A11	CLKHN	A11	M6	VCC2V5	M6
GND	A12	GND	A12	M5	VCC2V5	M5
VOP2_D6	A13	VOP2_D6	A13	M4	GND	M4
VON1_B6	A14	VON1_B6	A14	M3	GND	M3
VOP1_D6	A15	VOP1_D6	A15	M2	VOP2_A3	M2
GND	A16	GND	A16	M1	VON2_A3	M1
GND	B1	GND	B1	L16	CLKN_DDR	L16
GND	B2	GND	B2	L15	CLKP_DDR	L15
VON1_A1	B3	VON1_A1	B3	L14	VCC2V5	L14
VON1_B1	B4	VON1_B1	B4	L13	VCC2V5	L13
VON2_A1	B5	VON2_A1	B5	L12	VCC2V5	L12
GND	B6	GND	B6	L11	VCC2V5	L11
GND	B7	GND	B7	L10	GND	L10
GND	B8	GND	B8	L9	GND	L9
GND	B9	GND	B9	L8	GND	L8
GND	B10	GND	B10	L7	VCC2V5	L7
GND	B11	GND	B11	L6	VCC2V5	L6
GND	B12	GND	B12	L5	VCC2V5	L5
VON2_D6	B13	VON2_D6	B13	L4	VOP1_A3	L4
VOP1_B6	B14	VOP1_B6	B14	L3	VON1_A3	L3
VON1_D6	B15	VON1_D6	B15	L2	VOP1_C3	L2
GND	B16	GND	B16	L1	VON1_C3	L1
VON1_C1	C1	VON1_C1	C1	K16	VOP1_A5	K16
VOP1_C1	C2	VOP1_C1	C2	K15	VON1_A5	K15
GND	C3	GND	C3	K14	VOP2_B5	K14
VCC33	C4	VCC33	C4	K13	VON2_B5	K13
VCC33	C5	VCC33	C5	K12	VCC33	K12
VCC33	C6	VCC33	C6	K11	VCC33	K11
VCM	C7	VCM	C7	K10	GND	K10
GND	C8	GND	C8	K9	GND	K9
GND	C9	GND	C9	K8	GND	K8
GND	C10	GND	C10	K7	GND	K7
GND	C12	GND	C12	K6	VCC44	K6
VON2_A6	C13	VON2_A6	C13	K5	VCC44	K5
VOP2_A6	C14	VOP2_A6	C14	K4	VOP1_D2	K4
VON2_B6	C15	VON2_B6	C15	K3	VON1_D2	K3
VOP2_B6	C16	VOP2_B6	C16	K2	VON2_D2	K2
VON2_D1	C17	VON2_D1	C17	K1	VOP2_D2	K1
VOP2_D1	C18	VOP2_D1	C18	J16	VOP2_A5	J16
GND	C19	GND	C19	J15	VON2_A5	J15
VCC33	C20	VCC33	C20	J14	VOP2_C5	J14
VCC33	C21	VCC33	C21	J13	VON2_C5	J13
VCC33	C22	VCC33	C22	J12	VCC33	J12
VCC33	C23	VCC33	C23	J11	VCC33	J11
VCC33	C24	VCC33	C24	J10	GND	J10
GND	C25	GND	C25	J9	GND	J9
GND	C26	GND	C26	J8	GND	J8
GND	C27	GND	C27	J7	GND	J7
GND	C28	GND	C28	J6	VCC44	J6
GND	C29	GND	C29	J5	VCC44	J5
GND	C30	GND	C30	J4	VOP1_B2	J4
GND	C31	GND	C31	J3	VON1_B2	J3
GND	C32	GND	C32	J2	VOP2_B2	J2
GND	C33	GND	C33	J1	VON2_B2	J1
VON2_C1	E1	VON2_C1	E1	S3	GND	S3
VOP2_C1	E2	VOP2_C1	E2	0	Molex_connector	0
GND	E3	GND	E3	1	RF	1
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VCC33	E5	VCC33	E5	1	RF	1
VCC33	E6	VCC33	E6	0	GND	0
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VCC33	E12	VCC33	E12	0	GND	0
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VCC33	E14	VCC33	E14	0	GND	0
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VON1_A6	E16	VON1_A6	E16	0	GND	0
VOP1_A6	E17	VOP1_A6	E17	1	RF	1
VON2_A2	F1	VON2_A2	F1	0	GND	0
VOP2_A2	F2	VOP2_A2	F2	1	RF	1
VON2_B1	F3	VON2_B1	F3	0	GND	0
VOP2_B1	F4	VOP2_B1	F4	1	RF	1
VCC44	F5	VCC44	F5	0	GND	0
VCC44	F6	VCC44	F6	1	RF	1
VCC44	F7	VCC44	F7	0	GND	0
VCC44	F8	VCC44	F8	1	RF	1
VCC44	F9	VCC44	F9	0	GND	0
VCC44	F10	VCC44	F10	1	RF	1
VCC44	F11	VCC44	F11	0	GND	0
VCC44	F12	VCC44	F12	1	RF	1
VOP2_C6	F13	VOP2_C6	F13	0	GND	0
VOP2_C6	F14	VOP2_C6	F14	1	RF	1
VON1_D5	F15	VON1_D5	F15	0	GND	0
VOP1_D5	F16	VOP1_D5	F16	1	RF	1
VON1_D1	G1	VON1_D1	G1	0	GND	0
VOP1_D1	G2	VOP1_D1	G2	1	RF	1
VON2_C2	G3	VON2_C2	G3	0	GND	0
VOP2_C2	G4	VOP2_C2	G4	1	RF	1
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VCC45	G6	VCC45	G6	1	RF	1
VCC45	G7	VCC45	G7	0	GND	0
VCC45	G8	VCC45	G8	1	RF	1
VCC45	G9	VCC45	G9	0	GND	0
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VCC45	G11	VCC45	G11	0	GND	0
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VCC45	G13	VCC45	G13	0	GND	0
VCC45	G14	VCC45	G14	1	RF	1
VON1_B5	G15	VON1_B5	G15	0	GND	0
VON1_B5	G16	VON1_B5	G16	1	RF	1
VON1_A2	H1	VON1_A2	H1	0	GND	0
VOP1_A2	H2	VOP1_A2	H2	1	RF	1
VON1_C2	H3	VON1_C2	H3	0	GND	0
VON1_C2	H4	VON1_C2	H4	1	RF	1
VCC45	H5	VCC45	H5	0	GND	0
VCC45	H6	VCC45	H6	1	RF	1
VCC45	H7	VCC45	H7	0	GND	0
VCC45	H8	VCC45	H8	1	RF	1
VCC45	H9	VCC45	H9	0	GND	0
VCC45	H10	VCC45	H10	1	RF	1
VCC45	H11	VCC45	H11	0	GND	0
VCC45	H12	VCC45	H12	1	RF	1
VCC45	H13	VCC45	H13	0	GND	0
VCC45	H14	VCC45	H14	1	RF	1
VON2_D5	H15	VON2_D5	H15	0	GND	0
VOP2_D5	H16	VOP2_D5	H16	1	RF	1

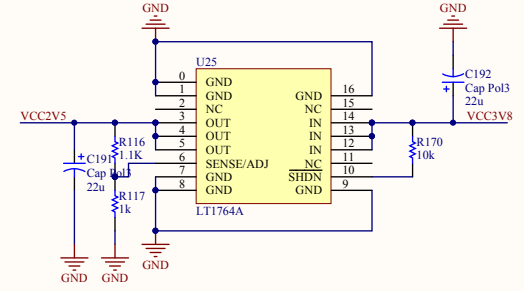
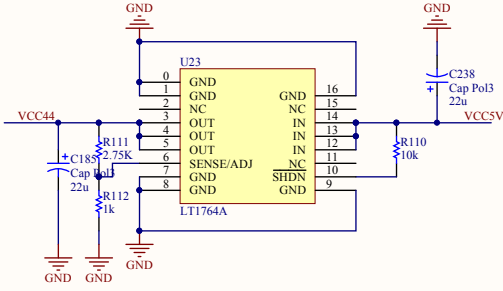
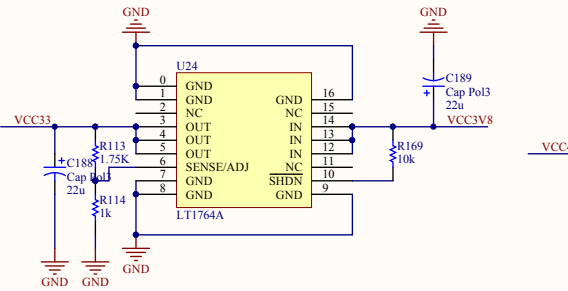
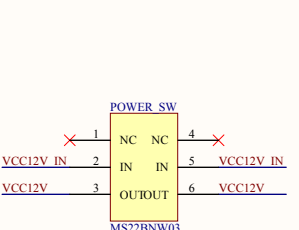
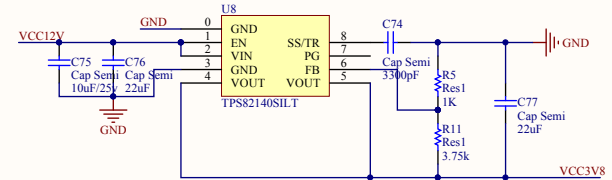
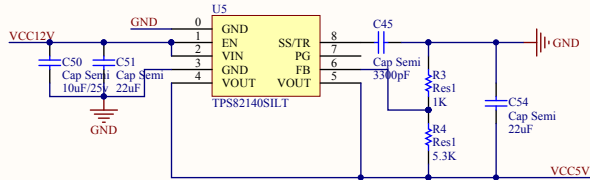
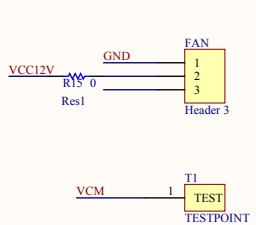


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Project Title: **AAD06S9000LEVM**

Sheet Title: **AAD06S9000L**

Size: A3	Revision: Rev-2
DrawnBy: QXL	CheckedBy: ZL
Sheet 2 of 4	Date: 2019/4/28



U7A		A1		GND	
B1	B1	A1	A2	GND	GND
B2	B2	A2	A3	GND	GND
B3	B3	A3	A4	GND	GND
B4	B4	A4	A5	GND	GND
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B37	B37	A37	A38	GND	GND
B38	B38	A38	A39	GND	GND
B39	B39	A39	A40	GND	GND
B40	B40	A40			

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U7B		C1		GND	
D1	D1	C1	C2	GND	GND
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D3	D3	C3	C4	GND	GND
D4	D4	C4	C5	GND	GND
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D37	D37	C37	C38	GND	GND
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D39	D39	C39	C40	GND	GND
D40	D40	C40			

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F4	F4	E4	E5	GND	GND
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H39	H39	G39	G40	GND	GND
H40	H40	G40			

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K4	K4	J4	J5	GND	GND
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K37	K37	J37	J38	GND	GND
K38	K38	J38	J39	GND	GND
K39	K39	J39	J40	GND	GND
K40	K40	J40			

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Project Title: **AAD06S9000LEVM**

Sheet Title: **FMC**

Size: **A3** Revision: **Rev-2.0**

DrawnBy: **QXL** CheckedBy: **ZL**

Sheet **3** of **4** Date: **2019/4/28**

